

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Toru TANZAWA et al.

Serial No.: Divisional of 09/953,227, filed
September 17, 2001

Filed: Herewith

For: NON-VOLATILE SEMICONDUCTOR
MEMORY

Atty. Docket No.: 001701.00240

Group Art Unit: Unknown

Examiner: Unknown

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with Applicants' duty of disclosure, the following information is submitted for consideration by the United States Patent and Trademark Office in connection with the above-captioned application. The information is identified on the attached PTO-1449 form.

This application relies, under 35 U.S.C. § 120, on the earliest filing date of prior U.S. patent application serial no. 09/953,227, filed September 17, 2001. The documents identified on the attached PTO 1449 form were submitted to and/or cited by the Office in a prior application and, therefore, copies are not required to be provided in this application. (See 37 C.F.R. § 1.98(d)).

Applicants do not waive any right to take appropriate action to establish patentability over the listed documents should they be applied as references against the claims of the present application.

It is respectfully requested that the Examiner fully consider each of the documents, initial the enclosed Form PTO-1449 in the appropriate place to indicate that the document has been

considered, and return a copy of the initialed form to the undersigned in accordance with MPEP Section 609.

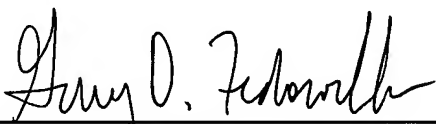
Applicants believe that no fee is necessary pursuant to 37 C.F.R. § 1.97(b). However, if a fee is due, the Office is authorized to charge Deposit Account No. 19-0733.

Respectfully submitted,

BANNER & WITCOFF, LTD.

Dated: September 22, 2003

By:



Gary D. Fedorochko
Registration No. 35,509

1001 G Street, N.W.
Washington, D.C. 20001-4597
(202) 508-9100

GDF:lab

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PTO/SB/08A (08-00)

Approved for use through 10/31/2002. OMB 0551-0031
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Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 1

Complete if Known

Application Number	TBA
Filing Date	Herewith
First Named Inventor	Toru TANZAWA et al.
Group Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	001701.00240

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
		5,570,315		Tanaka et al.	10/1996	
		5,652,719		Tanaka et al.	07/1997	

FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ₆
		Office ³	Number ⁴	Kind Code ⁵ (if known)				

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		M. BAUER, et al., "A Multilevel-cell 32Mb Flash Memory" 1995 IEEE International Solid-State Circuits Conference, pp. 132-133	
		M. OHKAWA, et al., "A 98mm ² 3.3V 64Mb Flash Memory with FN-NOR Type 4-level Cell" 1996 IEEE International Solid-State Circuits Conference, pp. 36-37	
		G. CAMPARDO, et al. "A 40mm ² 3V 50MHz 64Mb 4-Level Cell NOR-Type Flash Memory", 2000 IEEE International Solid-State Circuits Conference, pg. 274	

Examiner
Signature

Date
Considered

¹EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.